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This survey describes research directions in techniques to improve the r programming language. The standard technique for Java execution is in portability of programs. ...

Keywords: Java, Java virtual machine, bytecode-to-source translators. interpreters, just-in-time compilers

2 Pin: building customized program analysis tools with dynamic instrur Chi-Keung Luk, Robert Cohn, Robert Muth, Harish Patil, Artur Klauser, Geo Reddi, Kim Hazelwood

June 2005 PLDI '05: Proceedings of the 2005 ACM SIGPLAN conference implementation

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Robust and powerful software instrumentation tools are essential for properformance evaluation, and bug detection. To meet this need, we have called Pin. Our goals are ...

Keywords: dynamic compilation, instrumentation, program analysis to

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June 2005 SIGPLAN Notices Volume 40 Issue 6

3 LLVA: A Low-level Virtual Instruction Set Architecture Vikram Adve, Chris Lattner, Michael Brukman, Anand Shukla, Brian Gaeke December 2003 MICRO 36: Proceedings of the 36th annual IEEE/ACM Inte Publisher: IEEE Computer Society

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A virtual instruction set architecture (V-ISA) implementedvia a processor provide great flexibility to processor designers. Recentexamples such as existing hardware instruction ...

4 A simplified java bytecode compilation system for resource-constrair Carmen Badea, Alexandru Nicolau, Alexander V. Veidenbaum

September 2007 CASES '07: Proceedings of the 2007 international conference for embedded systems

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Embedded platforms are resource-constrained systems in whichperform code are ofcritical importance. However, standard techniques such as fu adaptive optimization (AO) may ...

Keywords: adaptive optimization, embedded systems, java virtual mac superoperators

- 5 Communications of the ACM: Volume 51 Issue 8
- August 2008 Communications of the ACM Publisher: ACM

Full text available: Digital Edition , Pof (6.85 MB) Additional Information: full citation

Bibliometrics: Downloads (6 Weeks): 299, Downloads (12 Months): 2192, Dov

- 6 A dynamic binary instrumentation engine for the ARM architecture [Min. Hazelwood, Artur Klauser]
 - October 2006 CASES '06: Proceedings of the 2006 international conference embedded systems

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Dynamic binary instrumentation (DBI) is a powerful technique for analy numerous DBI frameworks have been developed for general-purpose ar embedded architectures has been ...

Keywords: binary instrumentation, dynamic translation, embedded arc

- 7 Dynamic metrics for java
- Bruno Dulour, Karel Driesen, Laurie Hendren, Clark Verbrugge

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In order to perform meaningful experiments in optimizing compilation a usually rely on a suite of benchmark programs of interest to the optimiz are described as *numeric*, ...

Keywords: Java, dynamic metrics, execution traces, optimization, prof

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November 2003 SIGPLAN Notices Volume 38 Issue 11

- 8 Statistically rigorous java performance evaluation
- Andy Georges, Dries Buytaert, Lieven Eeckhout

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Java performance is far from being trivial to benchmark because it is affi application, its input, the virtual machine, the garbage collector, the her run-time causes ...

Keywords: benchmarking, data analysis, java, methodology, statistics

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9 HPC.NET - are CLI-based Virtual Machines Suitable for High Perform Werner Vogels

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The Common Language Infrastructure is a new, standardized virtual ma several platforms. In this paper we review whether this technology has community, for example by ...

10 LLVM: A Compilation Framework for Lifelong Program Analysis & Tr Chris Lattner, Vikram Adve

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Publisher: IEEE Computer Society
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This paper describes LLVM (Low Level Virtual Machine), a compiler frame lifelongprogram analysis and transformation for arbitrary programs, by a compiler transformations at compiler time....

11 Information systems outsourcing; a survey and analysis of the literation. Jens Dibbern, Tim Goles, Rudy Hirschheim, Bandula Jayatilaka

November 2004 SIGMIS Database . Volume 35 Issue 4

Publisher: ACM

Full text available: Pdi (1.51 MB) Additional Information: full extation, abs

Bibliometrics: Downloads (6 Weeks): 355, Downloads (12 Months): 3208, Dov

In the last fifteen years, academic research on information systems (IS) field of outsourcing research has grown so fast that there has been scar take a collective ...

Keywords: determinants, literature review, outcomes, outsourcing, rel foundations

12 Minimizing dependencies within generic classes for faster and small. Dan Tsafrir, Robert W. Wisniewski, David F. Bacon, Bjarne Stroustrup

October 2009 OOPSLA '09: Proceeding of the 24th ACM SIGPLAN conferen languages and applications

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Full text available: Pdf (604.18 KB) Additional Information: full citation, abs

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Generic classes can be used to improve performance by allowing compil compile-time polymorphism is narrower than that of runtime polymorph advocate a programming ...

Keywords: generalized hoisting, generics, scary assignments and initia

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October 2009 SIGPLAN Notices Volume 44 Issue 10

13 A fast, memory-efficient register allocation framework for embedded.

Sathyanarayanan Thammanur, Santosh Pande November 2004 Transactions on Programming Languages and System

Publisher: ACM Pequest Permissions

Full text available: Pdf (1.01 MB)

Additional Information: full citation, abs

Bibliometrics: Downloads (6 Weeks); 4. Downloads (12 Months); 67. Downloa In this work, we describe a "just-in-time," < i>usage density-based regi

systems with a limited general-purpose register set wherein speed, code concern. The main ...

Keywords: Code generation, compiler optimizations, compilers, dynam allocation

14 The ExoVM system for automatic VM and application reduction.

Ben L. Titzer, Joshua Auerbach, David F. Bacon, Jens Palsberg June 2007 PLDI '07: Proceedings of the 2007 ACM SIGPLAN conference

implementation

Publisher: ACM ♠ Request Permissions Full text available: ♣ Pdf (510.60 KB)

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Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 64, Downloa

Embedded systems pose unique challenges to Java application develope these challenges is the memory footprint of both the virtual machine an rapidly increasing ...

Keywords: VM design, VM modularity, dead code elimination, embedde initialization, static analysis, static compilation

Also published in:

June 2007 SIGPLAN Notices Volume 42 Issue 6

15 Communications of the ACM: Volume 52 Issue 10

October 2009 Communications of the ACM

Publisher: ACM
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Bibliometrics: Downloads (6 Weeks): 6420, Downloads (12 Months): 6420, Dc

16 Design of the Java HotSpot™ client compiler for Java 6

→ Thomas Kotzmann, Christian Wimmer, Hanspeter Mössenböck, Thomas Ro-May 2008 Transactions on Architecture and Code Optimization (T Publisher: ACM ⁴> Request Permissions

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Version 6 of Sun Microsystems' Java HotSpot™ VM ships with a redesign that includes several research results of the last years. The client compi by default ...

Keywords: Java, compiler, deoptimization, intermediate representation register allocation

17 Revisiting Out-of-SSA Translation for Correctness, Code Quality and Benoit Boissinot, Alain Darle, Fabrice Rastello, Benoit Dupont de Dinechin, March 2009 CGO '09: Proceedings of the 2009 International Symposium / Publisher: IEEE Computer Society

Full text available: Pdf (287.20 KB)

Additional Information: full citation, abs

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Static single assignment (SSA) form is an intermediate program repress be performed with fast and easy-to-implement algorithms. However, so where the SSA variables ...

Keywords: SSA form, Compilers, JIT-compilation

18 Using continuous statistical machine learning to enable high-speed paints instruction-/cycle-accurate instruction set simulators

Daniel Christopher Powell, Björn Franke

October 2009 CODES+1SSS '09: Proceedings of the 7th IEEE/ACM internal codesign and system synthesis

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Functional instruction set simulators perform instruction-accurate simula

Unlike their slower, but cycle-accurate counterparts however, they are r the higher level ...

Keywords: continuous statistical machine learning, instruction set simu

19 Application of a multi-criteria simulation optimization based DSS A. Azadeh, S. F. Ghaderi, A. Dabbaghi, M. Dehghanbaghi

July 2007 SCSC: Proceedings of the 2007 summer computer simulation Publisher: Society for Computer Simulation International Full text available: 實質 (209.58 KB) Additional Information: full citation, abs

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In this study we propose a decision support system for a textile dyeing, simulation model itself, enables managers to find a schedule of jobs on decision parameters) ...

Keywords: goal programming, metamodel, multi-criteria decision makifactors

20 Online performance auditing: using hot optimizations without getting Jeremy Lau, Matthew Arnold, Michael Hind, Brad Calder

June 2006 PLDI '06: Proceedings of the 2006 ACM SIGPLAN conference implementation

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As hardware complexity increases and virtualization is added at more la performance impact of optimizations becomes increasingly difficult. Proc substantial development ...

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